



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yang, *et al.*

Attorney Docket: TSM03-0589

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Examiner: TBD

Serial No.: 10/725,234

Art Unit: 2811

Title: Multilayered Dual Bit Memory Device with Improved Write/Erase Characteristics and Method of Manufacturing

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination on the merits, Applicants respectfully submit this Preliminary Amendment as set forth below: